

| L Number | Hits | Search Text | DB | Time stamp |
|-------------|-------|-----------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------|---------------------|
| 1 | 19365 | strobe adj (signal or line) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 08:45 |
| 2 | 4683 | (multiple or plural) adj clock | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 08:57 |
| 3 | 8241 | (cpu or processor or processing) adj clock | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:06 |
| 4 | 3459 | memory adj clock | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:06 |
| 5 | 21891 | first adj clock | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 08:59 |
| 6 | 20148 | second adj clock | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 08:59 |
| 7 | 386 | length with (strobe adj (signal or line)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 08:59 |
| 8 | 2229 | byte adj enabl\$3 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:00 |
| 9 | 255 | first adj (strobe adj (signal or line)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:01 |
| 10 | 228 | second adj (strobe adj (signal or line)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:01 |
| 11 | 4 | ((first adj (strobe adj (signal or line))) and "10") | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:01 |
| 12 | 166 | ((first adj (strobe adj (signal or line))) and (second adj (strobe adj (signal or line)))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:01 |
| 13 | 4 | ((first adj (strobe adj (signal or line))) and (second adj (strobe adj (signal or line)))) and (byte adj enabl\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:04 |
| 14 | 40 | third adj (strobe adj (signal or line)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:05 |

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| 15 | 27 | ((third adj (strobe adj (signal or line))) and ((first adj (strobe adj (signal or line))) and (second adj (strobe adj (signal or line)))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:05 |
| 16 | 2 | ((third adj (strobe adj (signal or line))) and ((first adj (strobe adj (signal or line))) and (second adj (strobe adj (signal or line)))) and (byte adj enabl\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:06 |
| 17 | 158 | (cpu or processor or processing) adj clk | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:06 |
| 18 | 13 | memory adj clk | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:06 |
| 19 | 27 | ((cpu or processor or processing) adj clk) or ((cpu or processor or processing) adj clock)) with (strobe adj (signal or line)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:08 |
| 20 | 25 | ((memory adj clock) or (memory adj clk)) with (strobe adj (signal or line)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:07 |
| 21 | 1 | ((cpu or processor or processing) adj clk) or ((cpu or processor or processing) adj clock)) with (strobe adj (signal or line)) and ((memory adj clock) or (memory adj clk)) with (strobe adj (signal or line)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:07 |
| 22 | 285 | ((cpu or processor or processing) adj clk) or ((cpu or processor or processing) adj clock)) and ((memory adj clk) or (memory adj clock)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:08 |
| 23 | 69 | (strobe adj (signal or line)) and (((cpu or processor or processing) adj clk) or ((cpu or processor or processing) adj clock)) and ((memory adj clk) or (memory adj clock)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:10 |
| 24 | 55 | length and ((strobe adj (signal or line)) and (((cpu or processor or processing) adj clk) or ((cpu or processor or processing) adj clock)) and ((memory adj clk) or (memory adj clock))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:11 |
| 25 | 64 | (length or size) and ((strobe adj (signal or line)) and (((cpu or processor or processing) adj clk) or ((cpu or processor or processing) adj clock)) and ((memory adj clk) or (memory adj clock))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:11 |
| 26 | 6 | (byte adj enabl\$3) and ((length or size) and ((strobe adj (signal or line)) and (((cpu or processor or processing) adj clk) or ((cpu or processor or processing) adj clock)) and ((memory adj clk) or (memory adj clock)))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/09/22 09:11 |

| L Number | Hits | Search Text | DB | Time stamp |
|----------|-------|--------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------|---------------------|
| 1 | 3343 | zlen or (zero adj length) or (no\$1 adj length) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:02 |
| 2 | 39 | (invalid\$5 or enabl\$3) with (zlen or (zero adj length) or (no\$1 adj length)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:02 |
| 3 | 2229 | byte adj enabl\$3 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:02 |
| 4 | 53 | (byte adj enabl\$3) with disabl\$3 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:02 |
| 5 | 1 | ((byte adj enabl\$3) with disabl\$3) same (quadword or (quad adj word)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:02 |
| 6 | 14597 | invalid\$5 with (memory or access or transfer or cycle) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:09 |
| 7 | 1 | ((byte adj enabl\$3) with disabl\$3) same (invalid\$5 with (memory or access or transfer or cycle)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:03 |
| 8 | 1227 | quadword or (quad adj word) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:04 |
| 9 | 610 | (invalid\$5 with (memory or access or transfer or cycle)) with enabl\$3 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:04 |
| 10 | 19365 | strobe adj (signal or line) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:04 |
| 11 | 87 | (strobe adj (signal or line)) and ((invalid\$5 with (memory or access or transfer or cycle)) with enabl\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:05 |
| 12 | 78 | (length or size) and ((strobe adj (signal or line)) and ((invalid\$5 with (memory or access or transfer or cycle)) with enabl\$3)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:05 |
| 13 | 76 | memory and ((length or size) and ((strobe adj (signal or line)) and ((invalid\$5 with (memory or access or transfer or cycle)) with enabl\$3))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:06 |
| 14 | 4759 | enabl\$3 with (strobe adj (signal or line)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:06 |

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| 15 | 80956 | enabl\$3 adj (signal or line) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:07 |
| 16 | 145 | ((enabl\$3 adj (signal or line)) with (invalid\$5 with (memory or access or transfer or cycle))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:08 |
| 17 | 4128 | (strobe adj (signal or line)) same (enabl\$3 adj (signal or line)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:08 |
| 18 | 42 | ((strobe adj (signal or line)) same (enabl\$3 adj (signal or line))) and ((enabl\$3 adj (signal or line)) with (invalid\$5 with (memory or access or transfer or cycle))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:12 |
| 19 | 20 | ((strobe adj (signal or line)) same (enabl\$3 adj (signal or line))) same ((enabl\$3 adj (signal or line)) with (invalid\$5 with (memory or access or transfer or cycle))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:09 |
| 20 | 6851 | invalid\$5 near3 (memory or access or transfer or cycle) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:11 |
| 21 | 39 | (invalid\$5 near3 (memory or access or transfer or cycle)) with (enabl\$3 adj (signal or line)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:11 |
| 22 | 2 | ((strobe adj (signal or line)) same (enabl\$3 adj (signal or line))) and ((invalid\$5 near3 (memory or access or transfer or cycle)) with (enabl\$3 adj (signal or line))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/09/22 10:12 |